IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): LAKSHMANAN,

Viswanathan et al.

Serial No.: 10/828,408

Confirmation No.: 4517

Filed: April 19, 2004

For: METHOD AND COMPUTER

PROGRAM FOR VERIFYING AN ()
INCREMENTAL CHANGE TO AN ()
INTEGRATED CIRCUIT DESIGN()

Art Unit: 2825

Examiner: Parihar, Suchin

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below.

September 1, 2006/ Eric James Whitesell /

Eric J. Whitesell #38657

AMENDMENT "B" UNDER 37 C.F.R. § 1.116

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Please enter the following response to the final Office Action mailed on August 15, 2006.

Status of the Claims pp. 2-5

Remarks pp. 6-10